Simulation

Model Suppliers

HDL Systems

Logic Modeling

Protocol (Zycad)

mips
Open RISC Technology

Simulation

Verilog Simulation Model

HDL Systems

Soft–RISC Verilog Simulation Models

MIPS® Chips Supported: R3000A, R3010®, IDT 3051/81™

Features:

- Full function behavioral models for system simulation and ASIC verification
- Real–time execution of assembly/diagnostic code and applications software
- Access to key internal registers for validation and debugging

Contact:

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(408) 522–2626 FAX
email: hayes@hdls.com

Hardware/ Software Models

Logic Modeling Systems

Hardware/Software Model Solutions

MIPS® Chips Supported: R4000®MC–50, R4000PC–50, R4000SC–50

Features:

SmartModel® Library, LM–family™ hardware model servers, SourceModel™ Libraries:

- Comprehensive approach to simulation modeling needs
- Broadest Device Coverage: Microprocessors, FPGAs, PLDs, DSPs, logic and memories
- Early Model Availability. Some models available pre–silicon
- All Models have intelligent error checking, full–timing, and consistent unknown handling to improve simulation accuracy.

Hosts: All Logic Modeling models provide full device functionality and timing information. Intelligent error checking immediately alerts the user to potential timing or usage violations. Logic Modeling supports all popular simulation environments.

Contact:

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Simulation

Model Bank

Protocol (Zycad)

Model Bank

MIPS® Chips Supported: R4400™/R4000®/R3000®/R3010®,
LSI LR33000, IDT3001/51™/51E/52™/52E,
Performance Semiconductor 3400

Features:

- Complete software/hardware simulation
- Operates on Zycad Hardware Accelerators
- Integrated with Vantage VSS, Simulation Technology VXI, and System Science VM products
- Identify functionality and timing errors
- Complete validation via Virtual Prototype

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